

Home | Login | Logout | Access Information | Alerts |

Welcome United States Patent and Trademark Office

☐ Search Session History

BROWSE SEARCH IEEE XPLORE GUIDE

Edit an existing query or compose a new query in the Search Query Display.

Select a search number (#)

- Add a query to the Search Query Display
- Combine search queries using AND, OR, or NOT
- Delete a search
- Run a search

Search Query Display

Recent Search Queries

#1 ((clocking system)<in>metadata)

Wed, 16 Nov 2005, 3:53:22 PM EST

- #2 ((algorithm <and> (clocking system))<in>metadata)
- #3 ((algorithm <and> (clocking system))<in>metadata)

Indexed by Inspec

Help Contact Us Privacy &

© Copyright 2005 IEEE -



☐ Search Result - Print Format

< Back

Key: IEEE JNL = IEEE Journal or Magazine, IEE JNL = IEE Journal or Magazine, IEEE CNF = IEEE Conference, IIEEE STD = IEEE Standard

1. Timing analysis including clock skew

Harris, D.; Horowitz, M.; Liu, D.; Computer-Aided Design of Integrated Circuits and Systems, IEEE Transactions on Volume 18, Issue 11, Nov. 1999 Page(s):1608 - 1618 IEEE JNL

2. Sequential and distributed simulations using Java threads

Azizi, M.; Aboulhamid, E.M.; Tahar, S.; Parallel Computing in Electrical Engineering, 2000. PARELEC 2000. Proceedings. International Conference on 27-30 Aug. 2000 Page(s):237 - 241

IEEE CNF

3. Synchronized Clock for DMS-100 Family

Munter, E.;
Communications, IEEE Transactions on [legacy, pre - 1988]
Volume 28, Issue 8, Aug 1980 Page(s):1276 - 1284
IEEE JNL

4. In search of the best clock

Allan, D.W.; Weiss, M.A.; Peppler, T.K.; Instrumentation and Measurement, IEEE Transactions on Volume 38, Issue 2, Apr 1989 Page(s):624 - 630 IEEE JNL

5. The derivation and experimental verification of clock synchronization theory

Palumbo, D.L.; Computers, IEEE Transactions on Volume 43, Issue 6, June 1994 Page(s):676 - 686 IEEE JNL

6. Zero skew clock routing in multiple-clock synchronous systems

Khan, W.; Hossain, M.; Sherwani, N.; Computer-Aided Design, 1992. ICCAD-92. Digest of Technical Papers., 1992 IEEE/ACM International Conference © 8-12 Nov. 1992 Page(s):464 - 467

IEEE CNF

7. A design approach for self-diagnosis of fault-tolerant clock synchronization

Lu, M.; Zhang, D.; Murata, T.; Computers, IEEE Transactions on Volume 38, Issue 9, Sept. 1989 Page(s):1337 - 1341 IEEE JNL

8. An improved numerical algorithm for calculating steady-state solutions of deterministic and stochastic Petronet models

Lindemann, C.;

Petri Nets and Performance Models, 1991. PNPM91., Proceedings of the Fourth International Workshop on 2-5 Dec. 1991 Page(s):176 - 185

IEEE CNF

9. Frequency steering of hydrogen masers

Breakiron, L.A.; Koppang, P.; Frequency Control Symposium, 1996. 50th., Proceedings of the 1996 IEEE International. 5-7 June 1996 Page(s):1113 - 1122

IEEE CNF

10. Smart clock: a new time

Weiss, M.A.; Allan, D.W.; Davis, D.D.; Levine, J.; Instrumentation and Measurement Technology Conference, 1992. IMTC '92., 9th IEEE 12-14 May 1992 Page(s):38 - 41

IEEE CNF

IEEE CNF

11. Phased logic: supporting the synchronous design paradigm with delay-insensitive circuitry

Linder, D.H.; Harden, J.H.; Computers, IEEE Transactions on Volume 45, Issue 9, Sept. 1996 Page(s):1031 - 1044 **IEEE JNL**

12. A robust single phase clocking for low power, high-speed VLSI applications

Afghahi, M.; Solid-State Circuits, IEEE Journal of Volume 31, Issue 2, Feb. 1996 Page(s):247 - 254 **IEEE JNL**

13. Clock: synchronizing internal relational storage with external XML documents

Xin Zhang; Mitchell, G.; Wang-Chien Lee; Rundensteiner, E.A.; Research Issues in Data Engineering, 2001. Proceedings. Eleventh International Workshop on 1-2 April 2001 Page(s):111 - 118 **IEEE CNF**

14. Retiming of circuits with single phase transparent latches

Shenoy, N.; Brayton, R.K.; Sangiovanni-Vincentelli, A.; Computer Design: VLSI in Computers and Processors, 1991. ICCD '91. Proceedings., 1991 IEEE International Conference on 14-16 Oct. 1991 Page(s):86 - 89

Indexed by #Inspec © Copyright 2005 IEEE -

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|----------|-------|---|---|---------------------|---------|------------------|
| L1 | 74624 | ("327"/\$.ccls. or "708"/\$.ccls.) and @ad<"20020226" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/16 15:27 |
| L2 | 2080 | 708/270-273.ccls. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/16 15:27 |
| L3 | 2025 | 2 and @ad<"20020226" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/16 15:27 |
| L4 | 118 | 1 and (lcm or (least adj common)) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/16 15:35 |
| L5 | 81 | 4 and frequenc\$3 and (reference\$1 or master) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/16 15:28 |
| L6 | 65 | 5 and divid\$3 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/16 15:28 |
| L7 | 54 | 6 and clock\$1 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/16 15:28 |
| L8 | 54 | 7 not 3 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/16 15:35 |

| L9 | 12 | 3 and (lcm or (least adj common)) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/16 15:35 |
|-----|-----|---|---|----|----|------------------|
| L10 | 9 | ("4468632" "5018170" "5224132").PN. OR ("6107843"). URPN. | US-PGPUB; USPAT; USOCR | OR | ON | 2005/11/16 15:41 |
| L11 | 19 | (REESE.in. or GILBERT.in.) and (Icm or (least adj common)) | US-PGPUB; USPAT; USOCR | OR | ON | 2005/11/16 15:41 |
| L12 | 0 | clock\$1 and frquenc\$3 and @ad<"20020226" and (lcm or (least adj common)) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/16 15:43 |
| L13 | 658 | clock\$1 and frequenc\$3 and @ad<"20020226" and (Icm or (least adj common)) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/16 15:43 |
| L14 | 22 | tier\$1 and 13 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/16 15:44 |
| L15 | 0 | 14 and lumpicm | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/16 15:45 |
| L16 | 2 | lumplcm | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/16 15:45 |
| L17 | 1 | ("2003/0163775").URPN. | USPAT | OR | ON | 2005/11/16 15:46 |
| L18 | 905 | 377/47-49.ccls. and @ad<"20020226" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/16 16:09 |

| L19 | 6 | 18 and (lcm or (least adj common)) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/16 16:11 |
|-----|----|--|---|----|----|------------------|
| L20 | 67 | ((clock\$1 and divid\$3).ti. or (clock\$1 and divid\$3).clm. or (clock\$1 and divid\$3).ab.) and @ad<"20020226" and (708/270-273.ccls. or "377".47-49. ccls.) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/16 16:11 |
| L21 | 1 | 20 and (Icm or (least adj common)) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/16 16:11 |
| S1 | 2 | lumplcm | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/16 14:43 |